

| | Type | Hits | Search Text |
|----|------|------|---|
| 1 | BRS | 8 | 257/ |
| 2 | BRS | 12 | 257/\$.ccls. and ((ESD) with (protection)) and ((transistors) and (resistors with (collectors or collector))) |
| 3 | BRS | 11 | 257/\$.ccls. and ((ESD) with (protection)) and ((transistors) and (resistors with (collectors or collector))) |
| 4 | BRS | 101 | 257/\$.ccls. and (ESD or (electrostatic adj protection)) and (transistors or transistor) and ((resistors or resistor) with (collectors or collector)) |
| 5 | BRS | 0 | 357/23.13.ccls. |
| 6 | BRS | 27 | 257/\$.ccls. and (ESD or (electrostatic adj protection)) and (transistors or transistor) and ((resistors or resistor) with (collectors or collector)) and ((grounded or ground) with (emitter with base)) |
| 7 | BRS | 1641 | 257/355,360,362.ccls. |
| 8 | BRS | 1641 | 257/355,360,362.ccls. |
| 9 | BRS | 1 | 257/\$.ccls. and (substrate) and (second adj (insulating or insulator)) and ((buffer or buffered) adj layer) or resistor) and (second adj (contact adj hole)) and (transistor) and (input with pad) |
| 10 | BRS | 0 | 257/\$.ccls. and (substrate) and (second adj (insulating or insulator)) and ((buffer or buffered) adj layer) or resistor) and (second adj (contact adj hole)) and (transistor) and (input with pad) |
| 11 | BRS | 406 | 257/355.ccls. |
| 12 | BRS | 4 | 257/355.ccls. and silicide |
| 13 | BRS | 100 | 257/355.ccls. and silicide |

| | DBs | Time Stamp | Comments | Error Definition |
|----|------------------------------|-----------------------------|----------|------------------|
| 1 | USPAT; US-PGPUB | 2002/02/05 09:06 | | |
| 2 | USPAT; US-PGPUB | 2002/02/05 09:10 | | |
| 3 | USPAT; EPO; JPO; DERWENT | 2002/02/05 09:12 | | |
| 4 | USPAT; EPO; JPO; DERWENT | 2002/02/05 09:33 | | |
| 5 | USPAT; EPO; JPO; DERWENT | 2002/02/05 09:32 | | |
| 6 | USPAT; EPO; JPO; DERWENT | 2002/02/05 09:40 | | |
| 7 | USPAT; EPO; JPO; DERWENT | 2002/02/05 09:40 | | |
| 8 | USPAT; EPO; JPO; DERWENT | 2002/02/05 12:54 | | |
| 9 | USPAT; EPO; JPO; DERWENT | 2002/02/05 10:38 | | |
| 10 | EPO; JPO; DERWENT | 2002/02/05 10:38 | | |
| 11 | EPO; JPO; DERWENT | 2002/02/05 12:36 | | |
| 12 | EPO; JPO; DERWENT | 2002/02/05 12:36 | | |
| 13 | USPAT; EPO; JPO; DERWENT | 2002/02/05 12:36 | | |

| | Errors |
|----|--------|
| 1 | 0 |
| 2 | 0 |
| 3 | 0 |
| 4 | 0 |
| 5 | 0 |
| 6 | 0 |
| 7 | 0 |
| 8 | 0 |
| 9 | 0 |
| 10 | 0 |
| 11 | 0 |
| 12 | 0 |
| 13 | 0 |

| | Type | Hits | Search Text |
|----|------|------|---|
| 14 | BRS | 11 | 257/\$.ccls. and ((ESD) with (protection)) and ((transistors) and (resistors with (collectors or collector))) |
| 15 | BRS | 11 | 257/\$.ccls. and ((ESD) with (protection)) and ((transistors) and (resistors with (collectors or collector))) |
| 16 | BRS | 444 | 257/364,365.ccls. |

| | DBs | Time Stamp | Comments | Error Definition |
|----|-----------------------------|------------------|----------|------------------|
| 14 | USPAT; EPO; JPO; DERWENT | 2002/02/05 12:49 | | |
| 15 | USPAT; EPO; JPO; DERWENT | 2002/02/05 12:49 | | |
| 16 | USPAT; EPO; JPO; DERWENT | 2002/02/05 12:55 | | |

| | Errors |
|----|---------------|
| 14 | 0 |
| 15 | 0 |
| 16 | 0 |